



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

2825
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TECHNOLOGY CENTER 2000
282
9-9-03

In re Application of

Bednar et al.

Serial No.: 10/065,201

Group Art Unit: 2825

Filed: September 25, 2002

Examiner: Unknown

For: VOLTAGE ISLAND CHIP IMPLEMENTATION

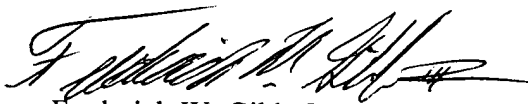
Assistant Commissioner of Patents
Washington, D.C. 20231

SUBMISSION OF FORMAL DRAWINGS

Sir:

Submitted herewith are fifteen (15) sheets of formal drawings comprising Figs. 1-16 for the above-referenced patent application. Acknowledgment of receipt is respectfully requested. Please substitute these formal drawings for the drawings which were filed with the application.

Respectfully Submitted,


Frederick W. Gibb, III
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CERTIFICATE OF MAILING BY FIRST CLASS MAIL (37 CFR 1.8)Applicant(s): **Bednar et al.**

Docket No.

BUR920020056

Serial No.

10/065,201

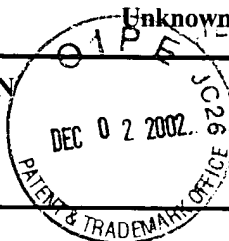
Filing Date

September 25, 2002

Examiner

Unknown**DEC -4****2002**

Group Art Unit

2825Invention: **VOLTAGE ISLAND CHIP IMPLEMENTATION**I hereby certify that this **Submission of Formal Drawings**

(Identify type of correspondence)

is being deposited with the United States Postal Service as first class mail in an envelope addressed to: The

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